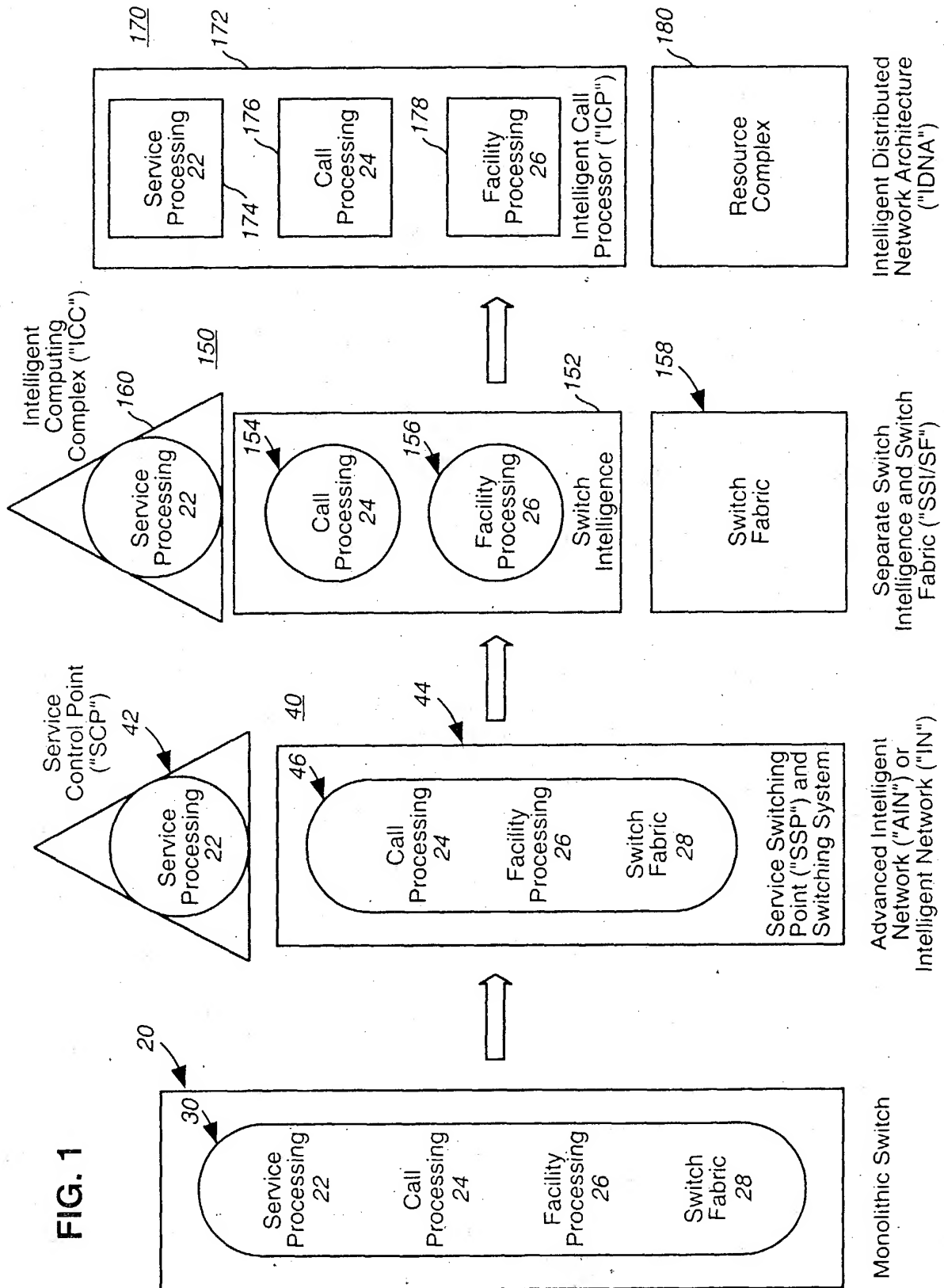
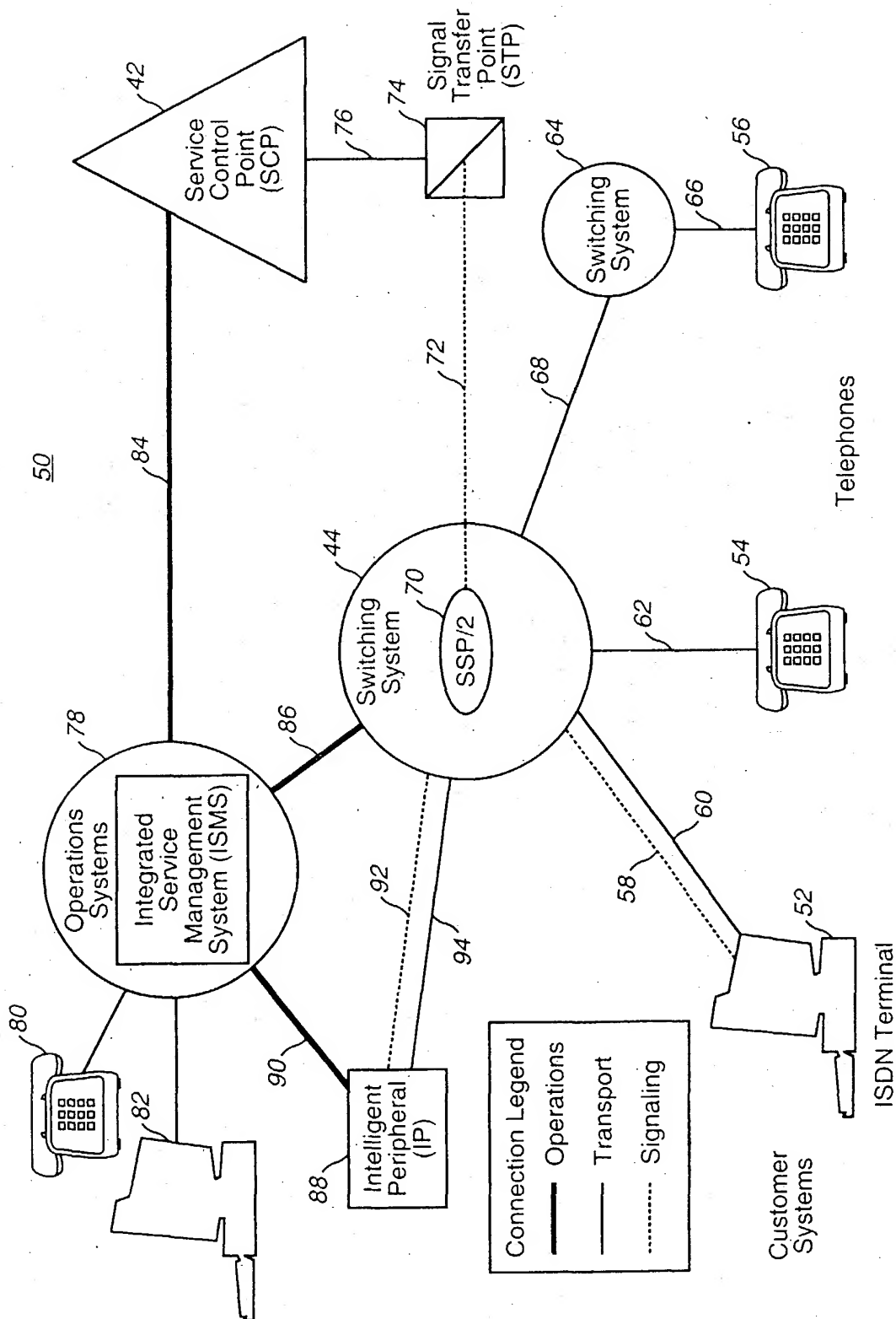


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Prior Art
FIG. 2

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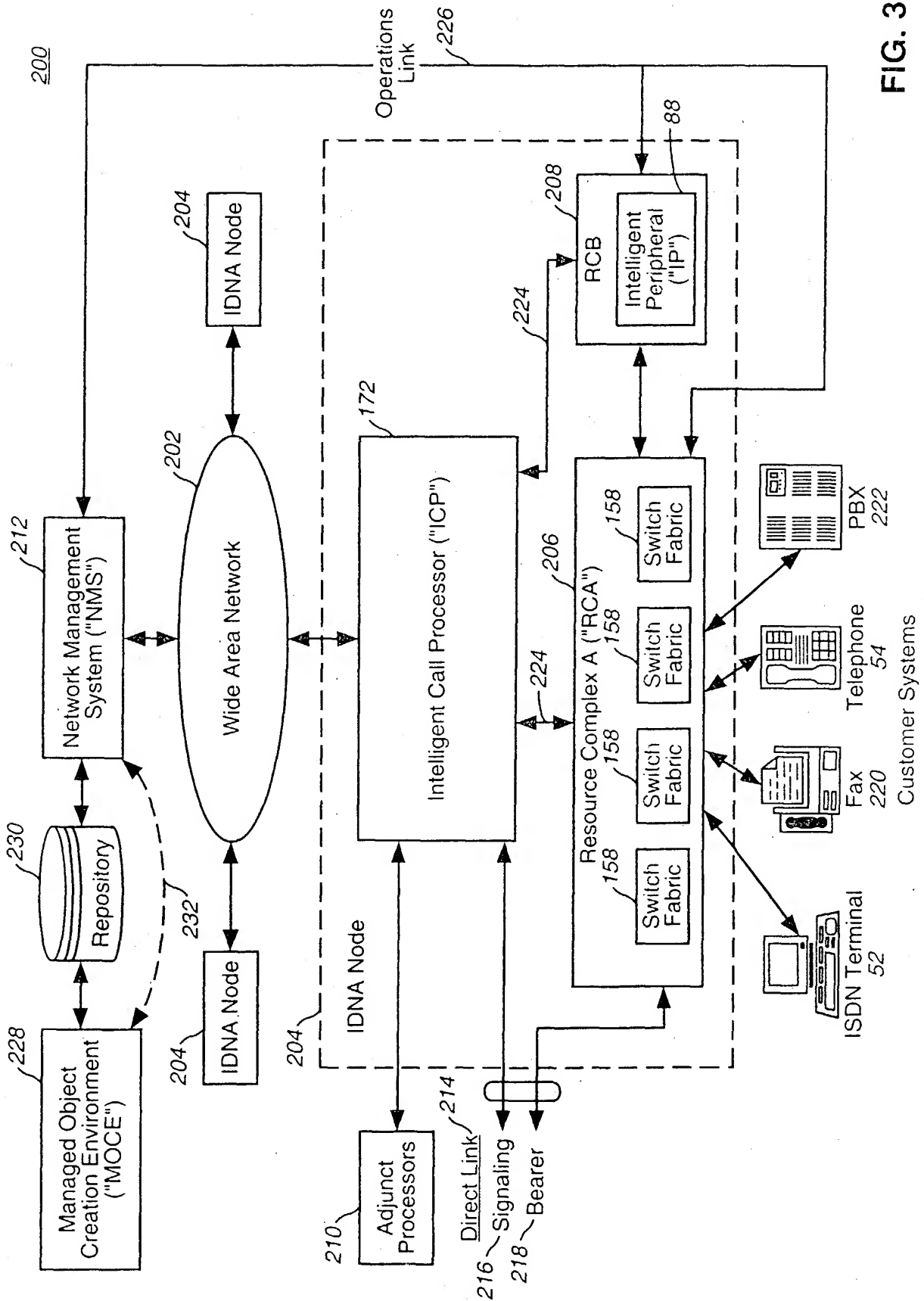


FIG. 3

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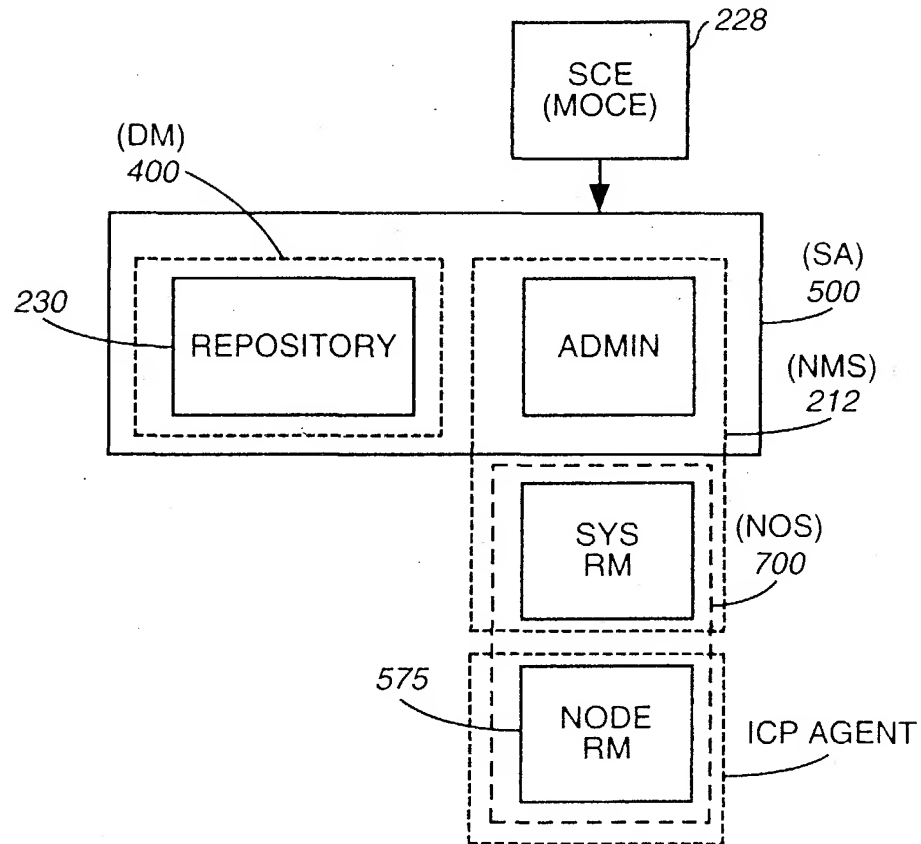
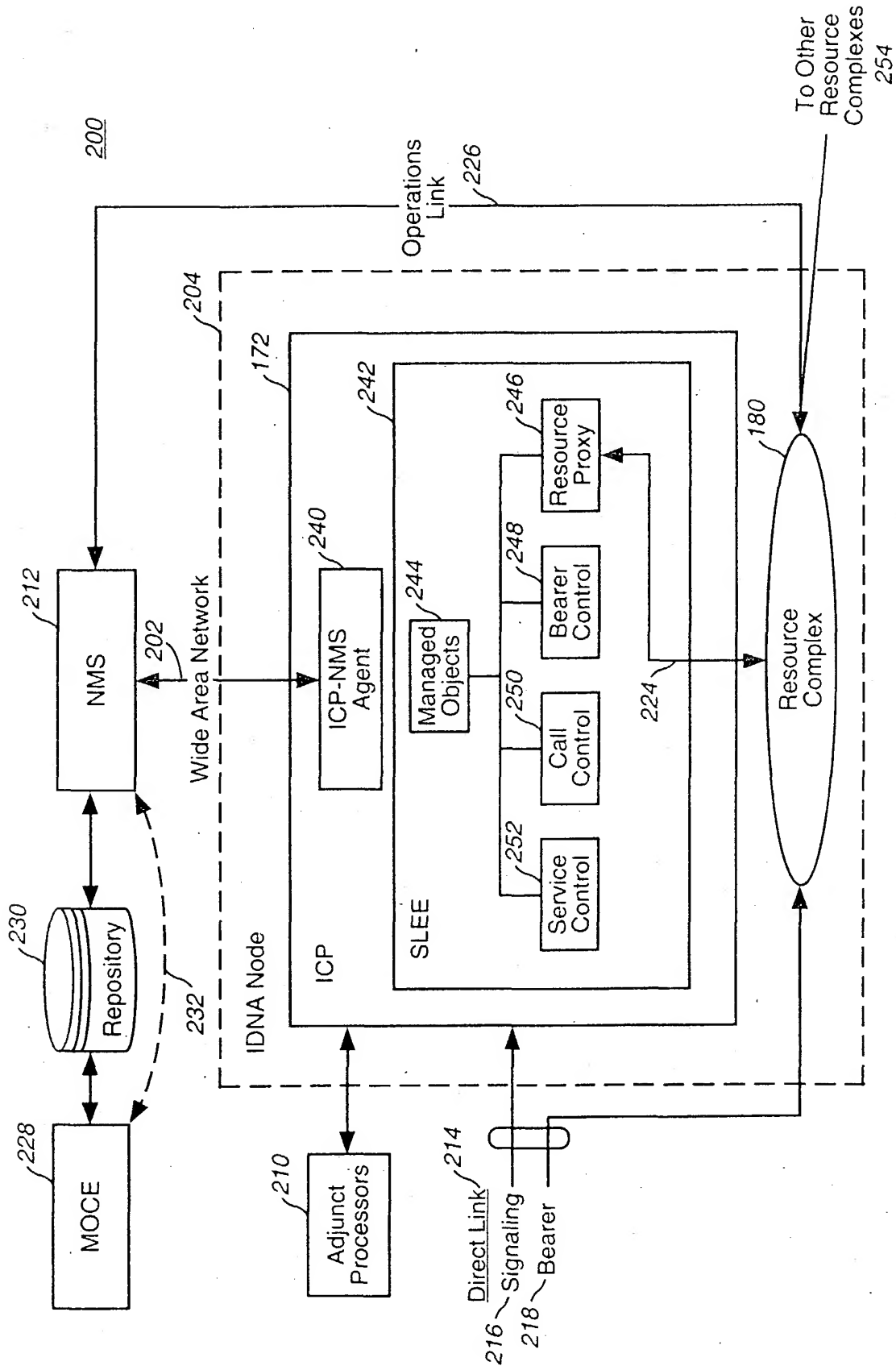


FIG. 4



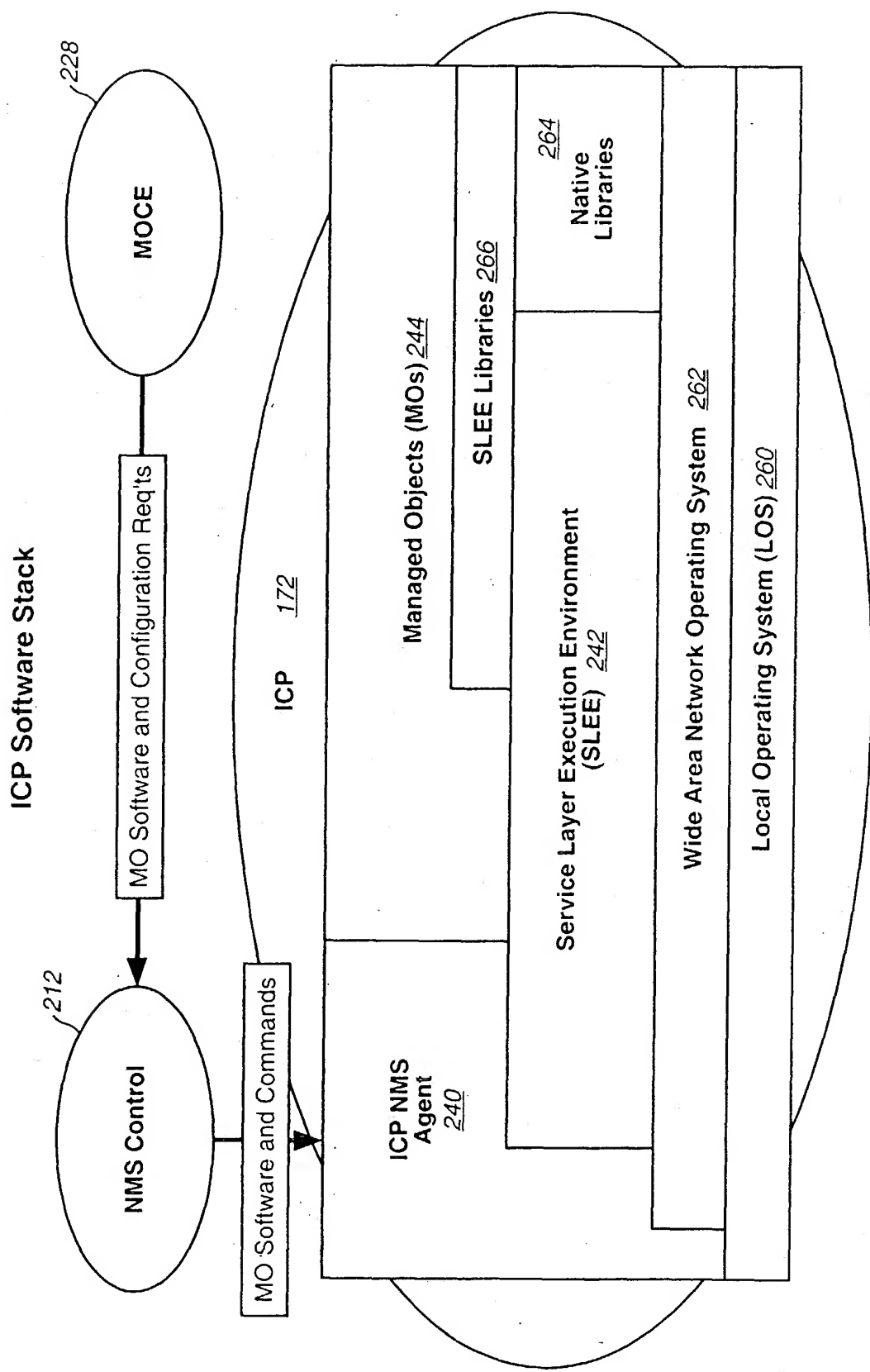


FIG. 6

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VM to SLEE

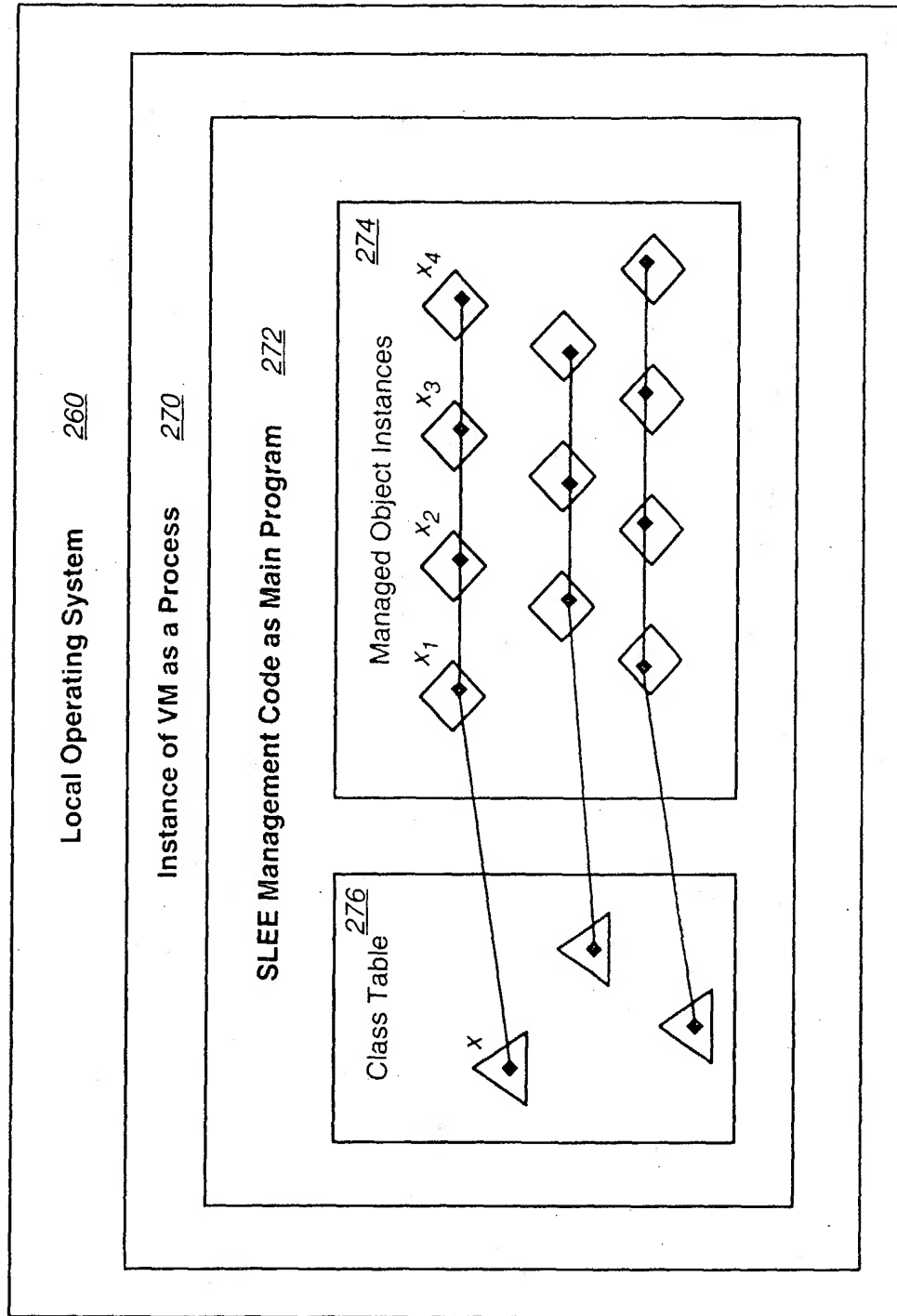


FIG. 7

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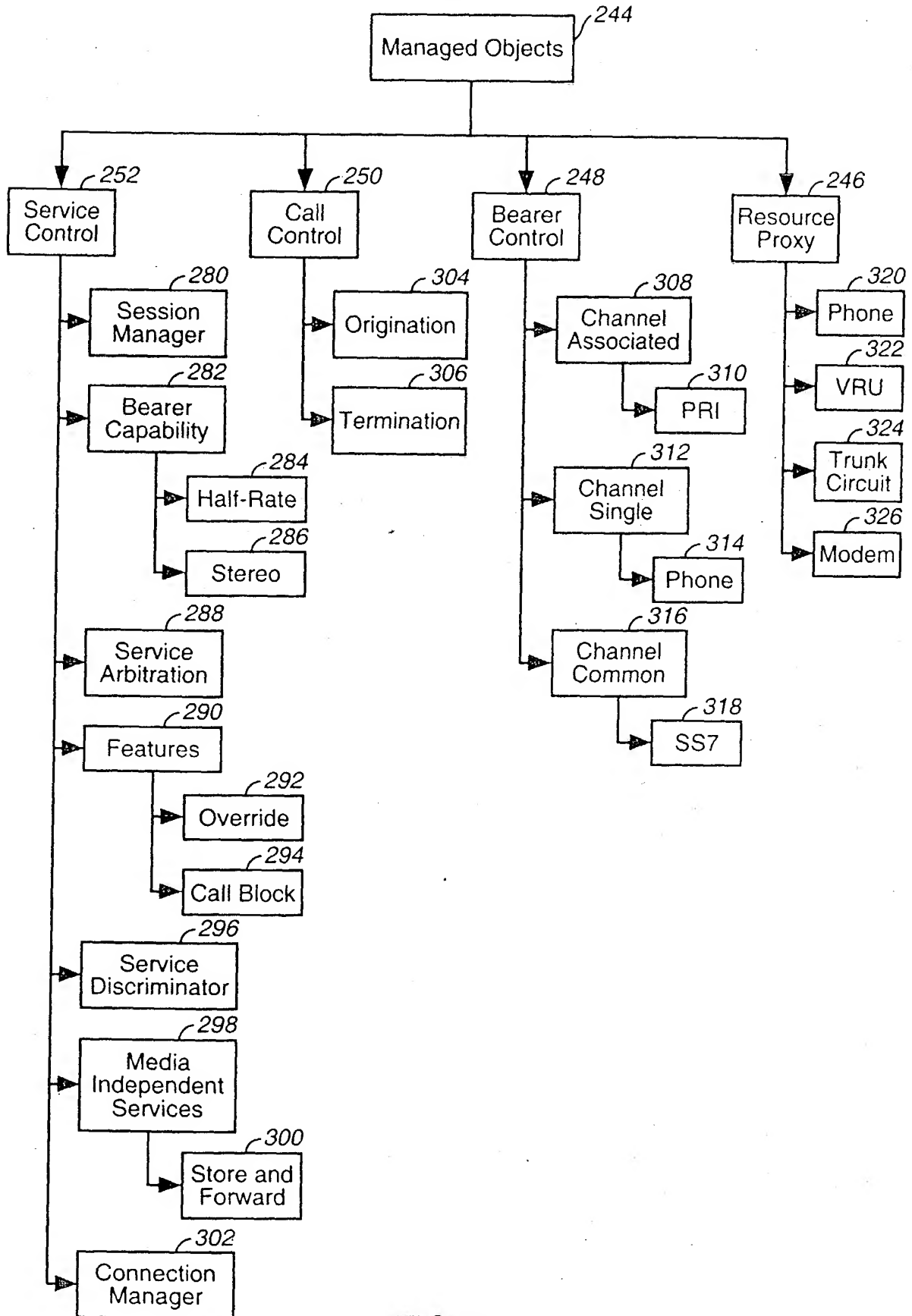


FIG. 8

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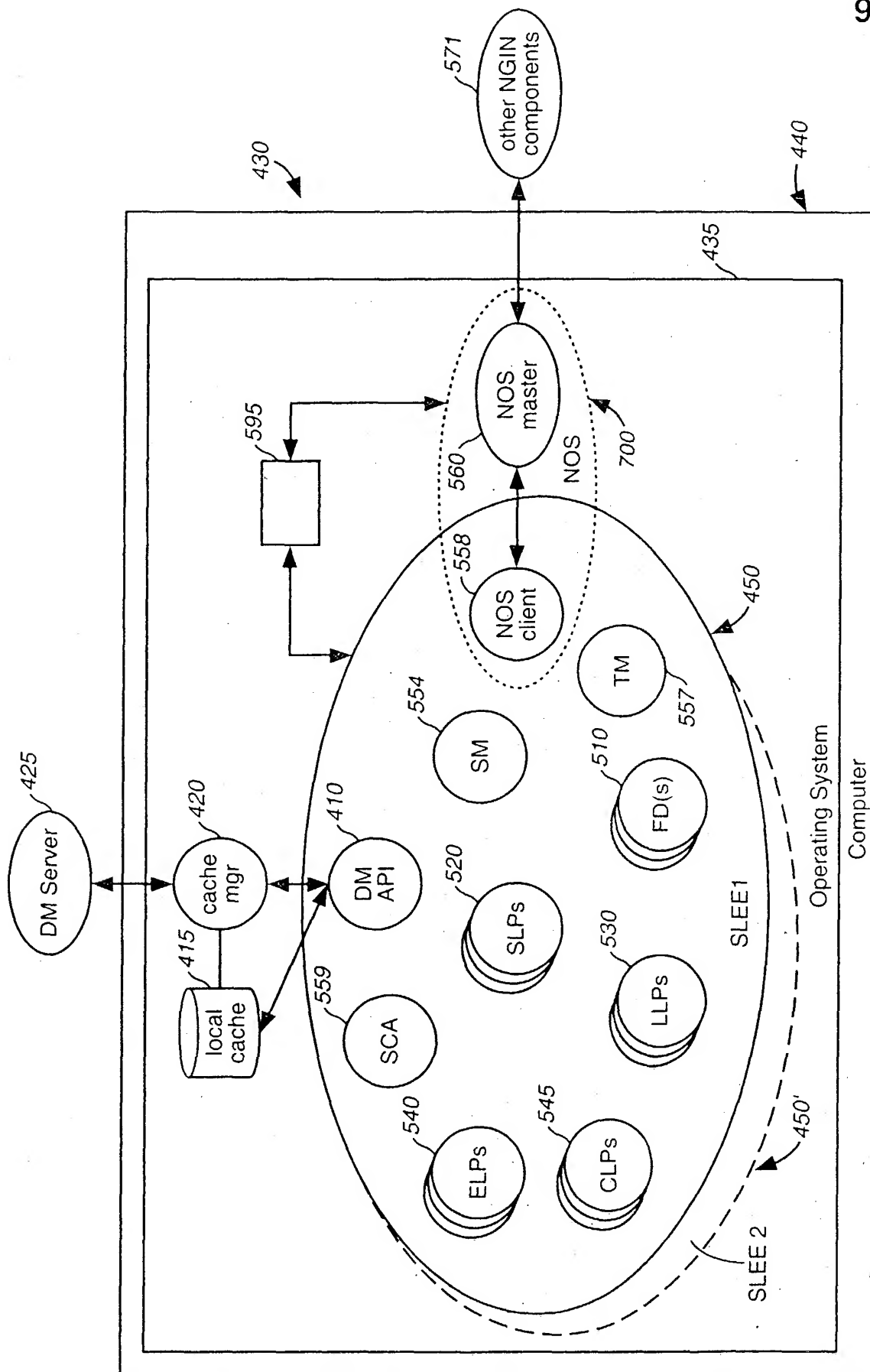


FIG. 9

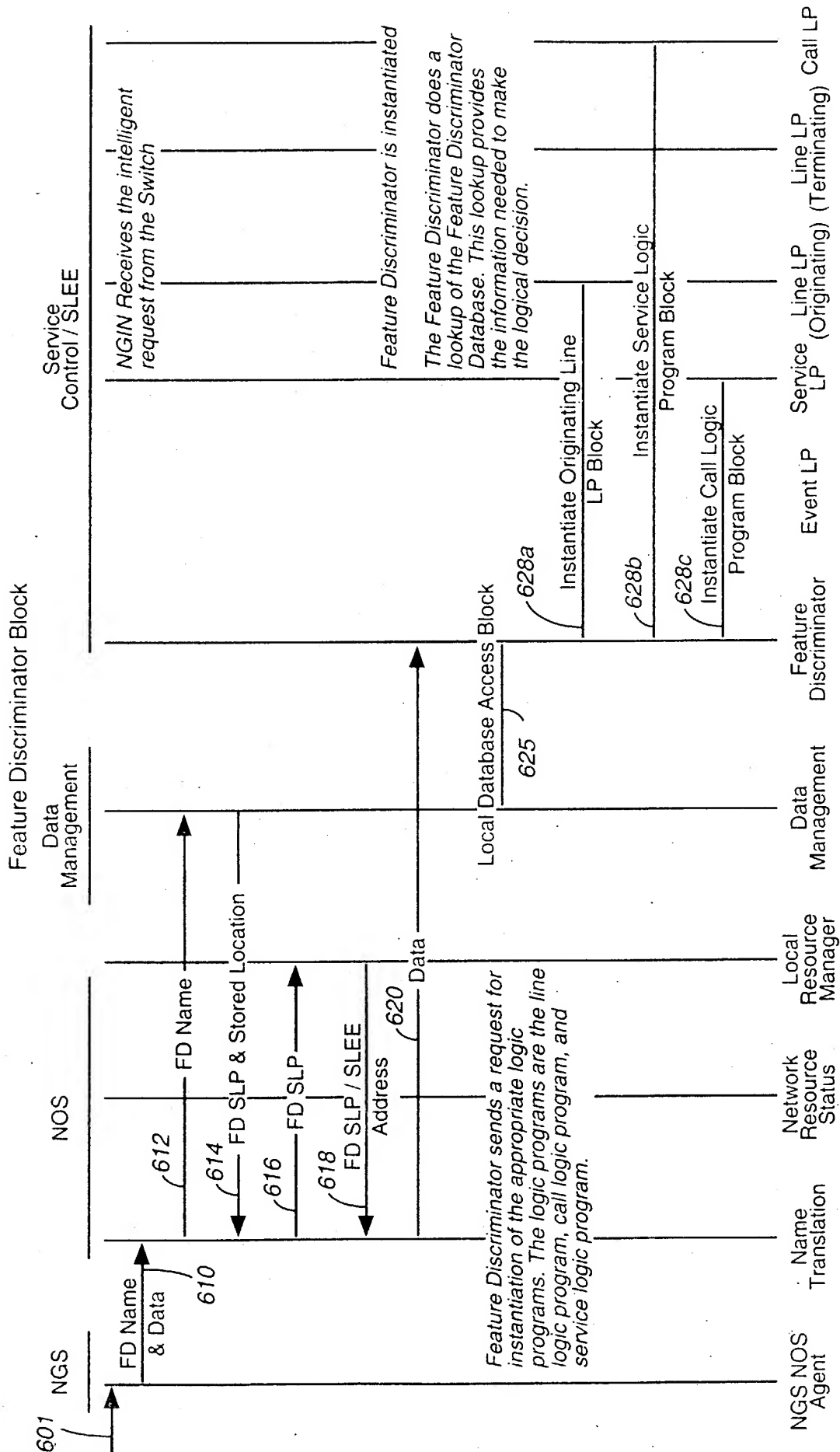


FIG. 10a

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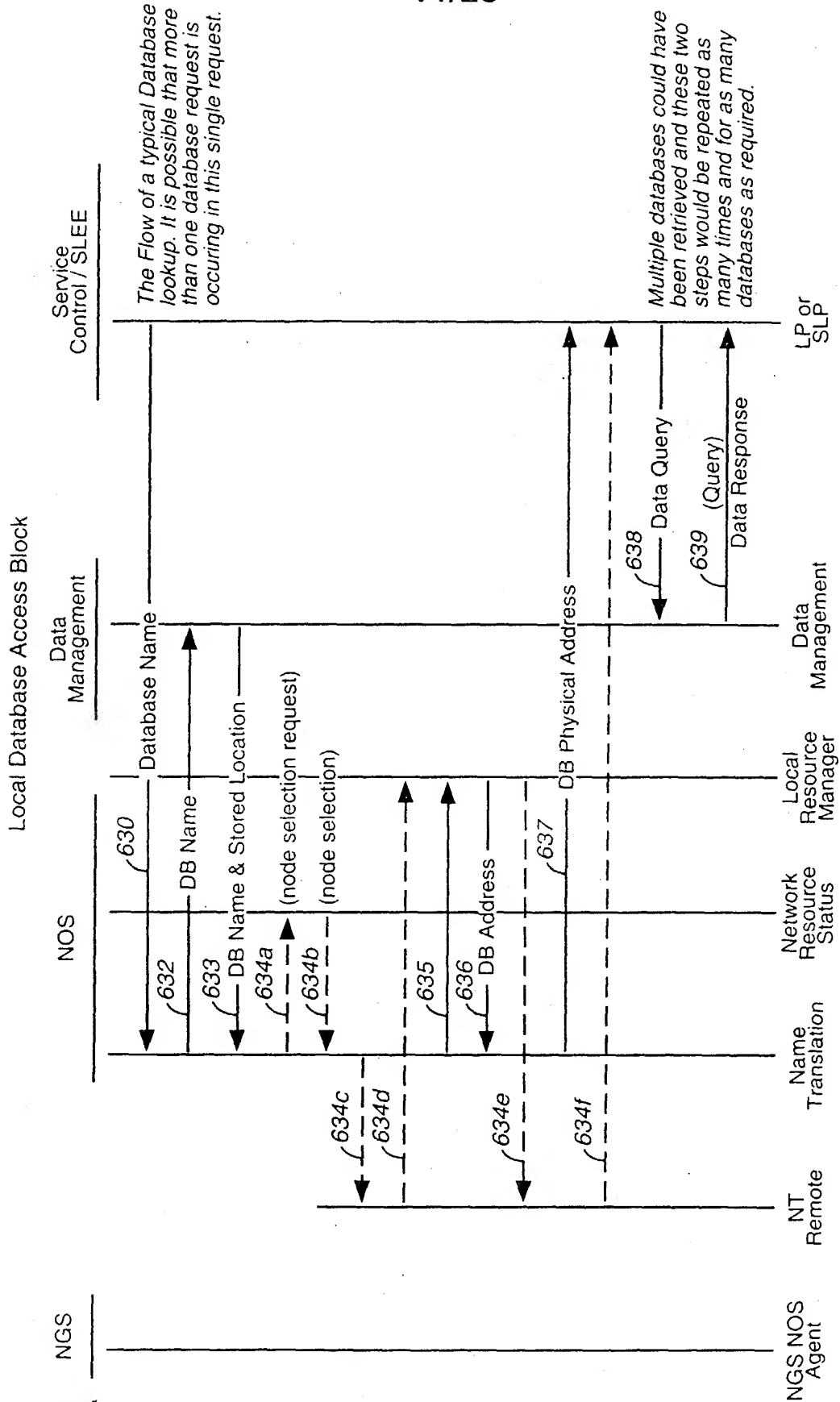


FIG. 10b

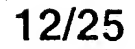


FIG. 10c

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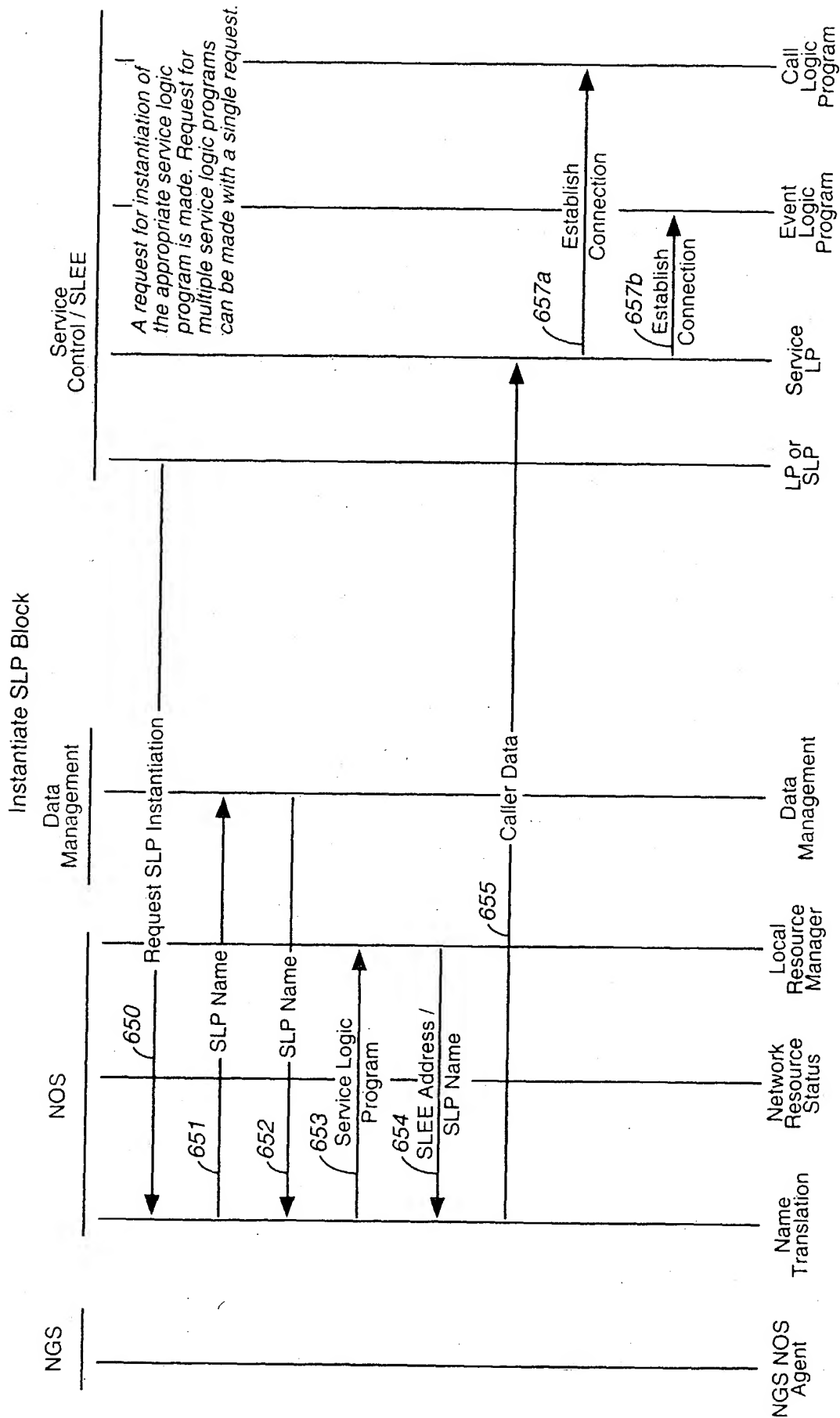
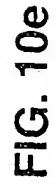


FIG. 10d



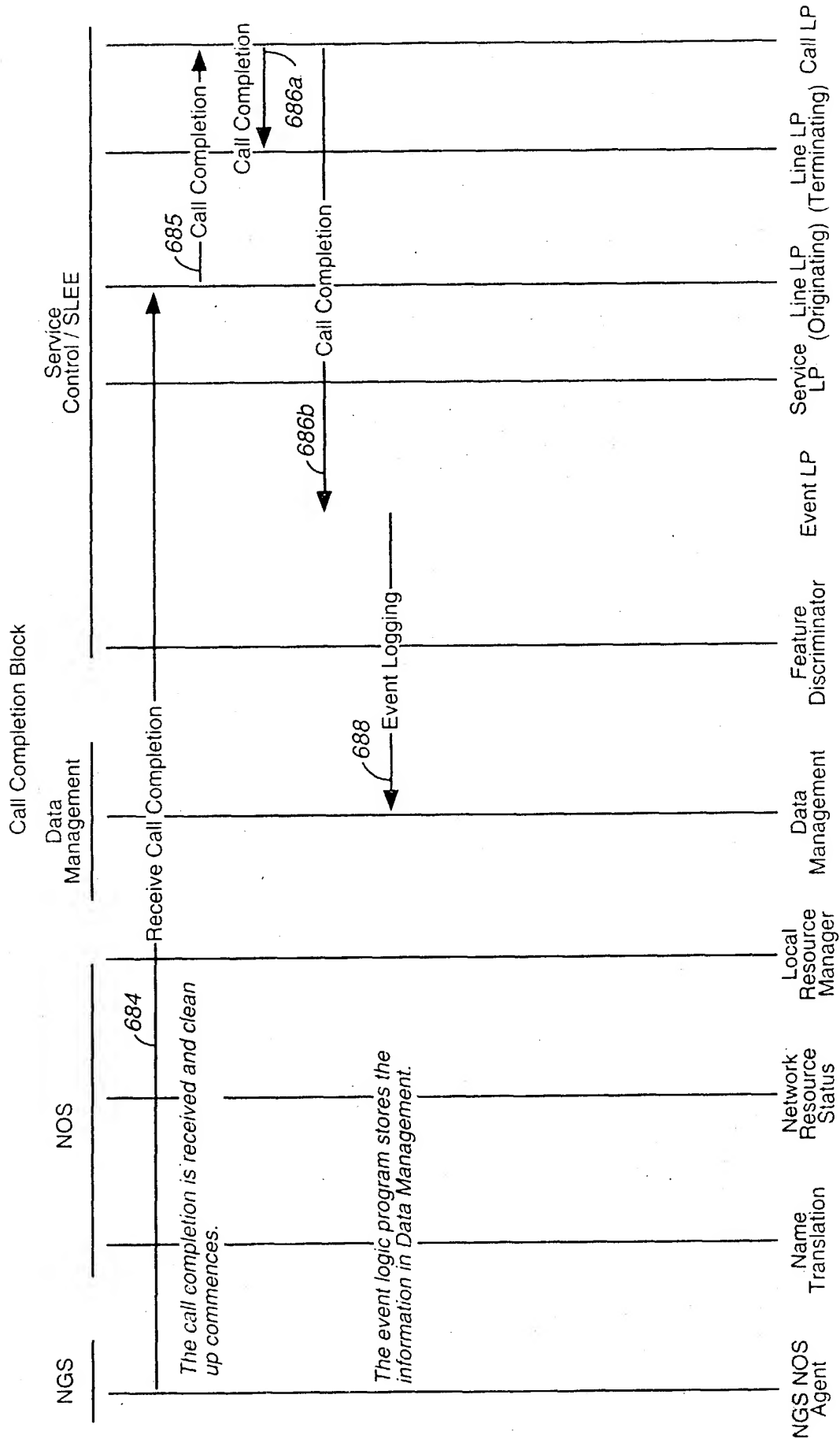


FIG. 10f

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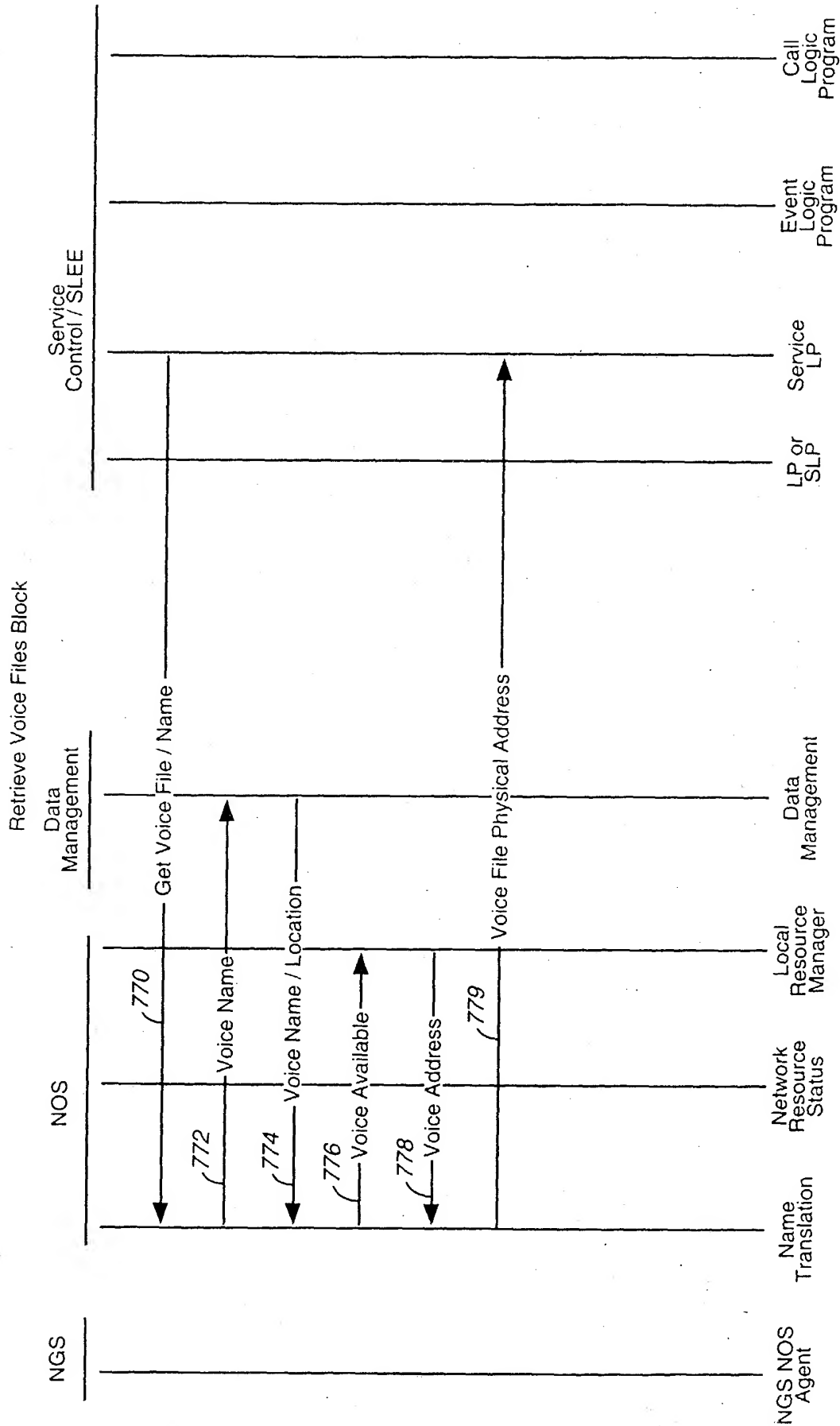


FIG. 10g

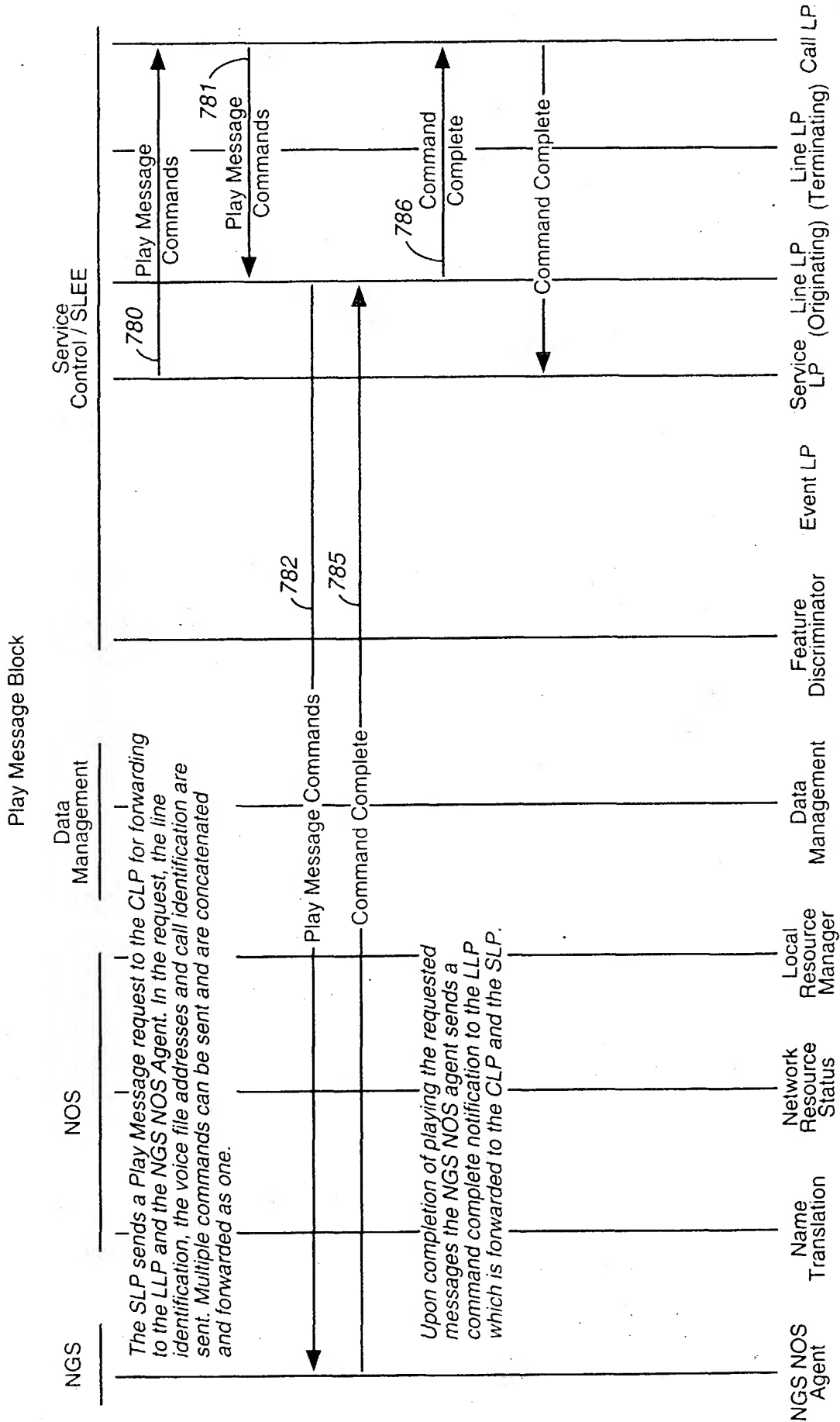


FIG. 10h

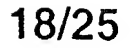
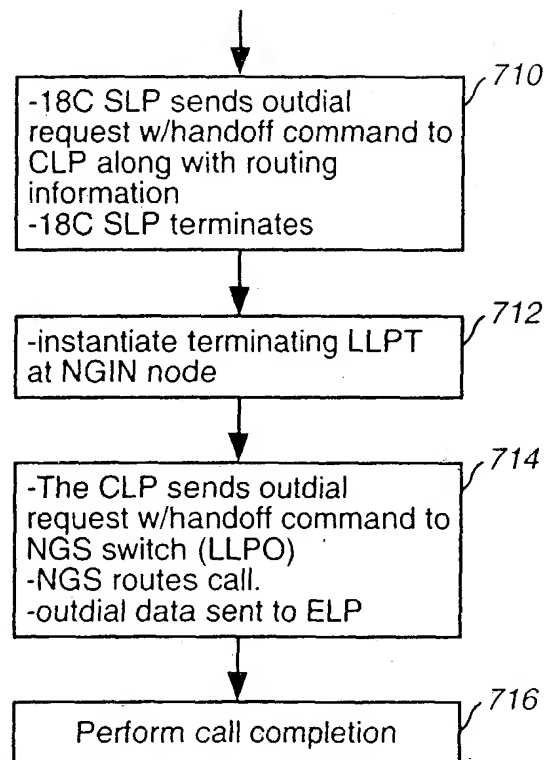
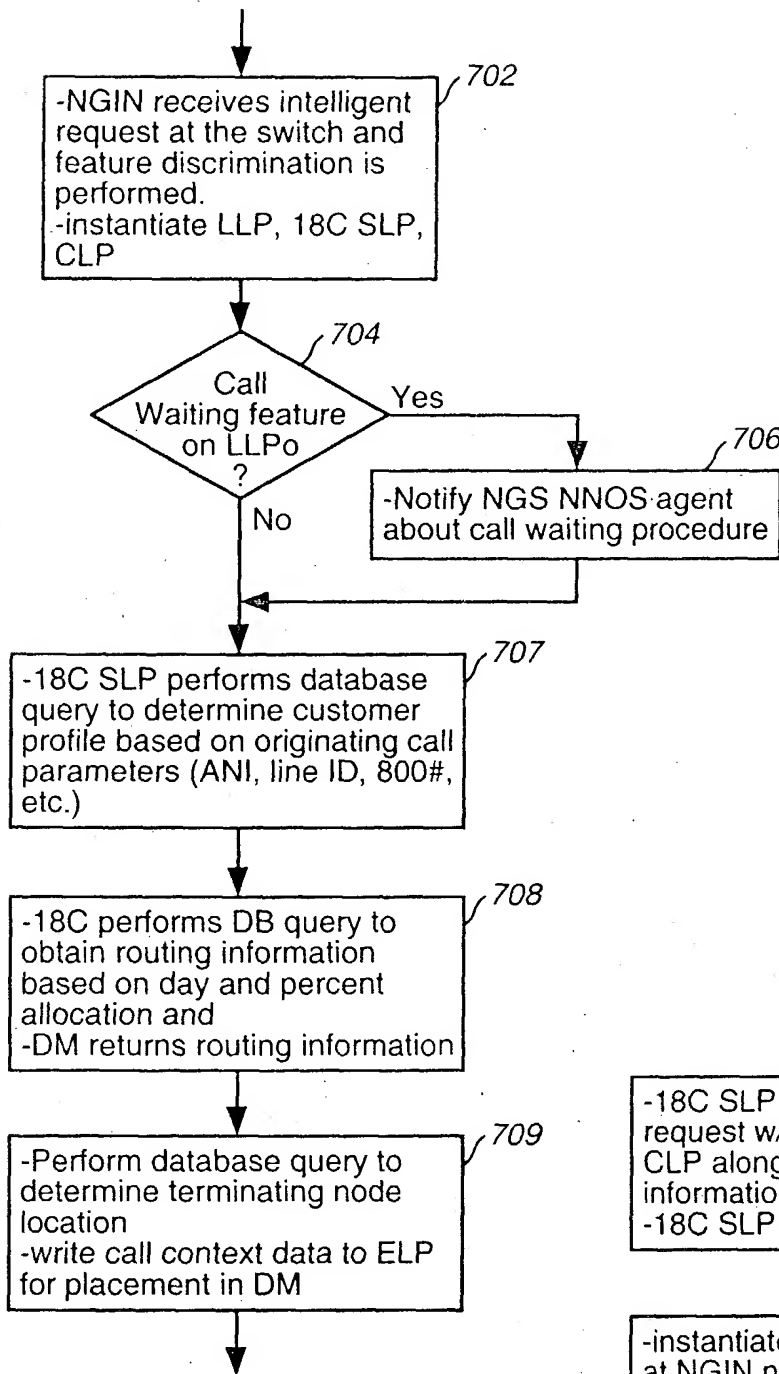


FIG. 10i

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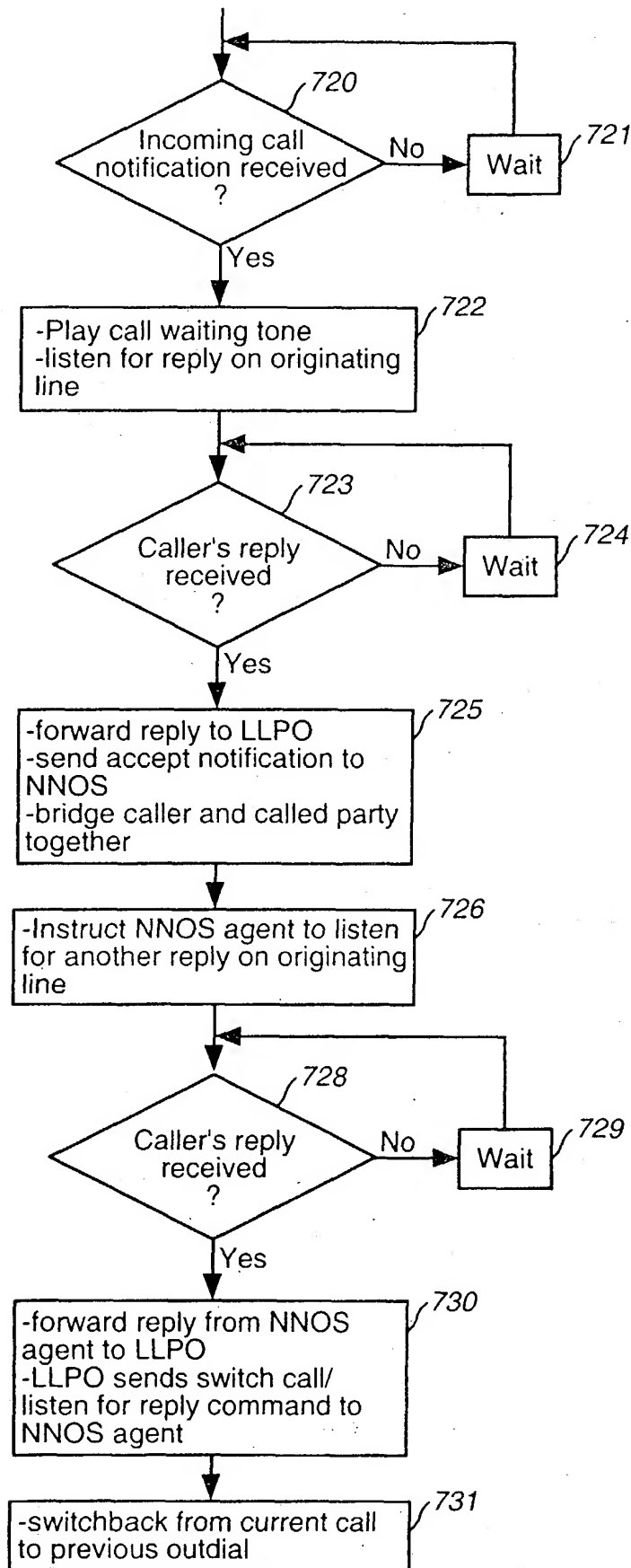


FIG. 11c

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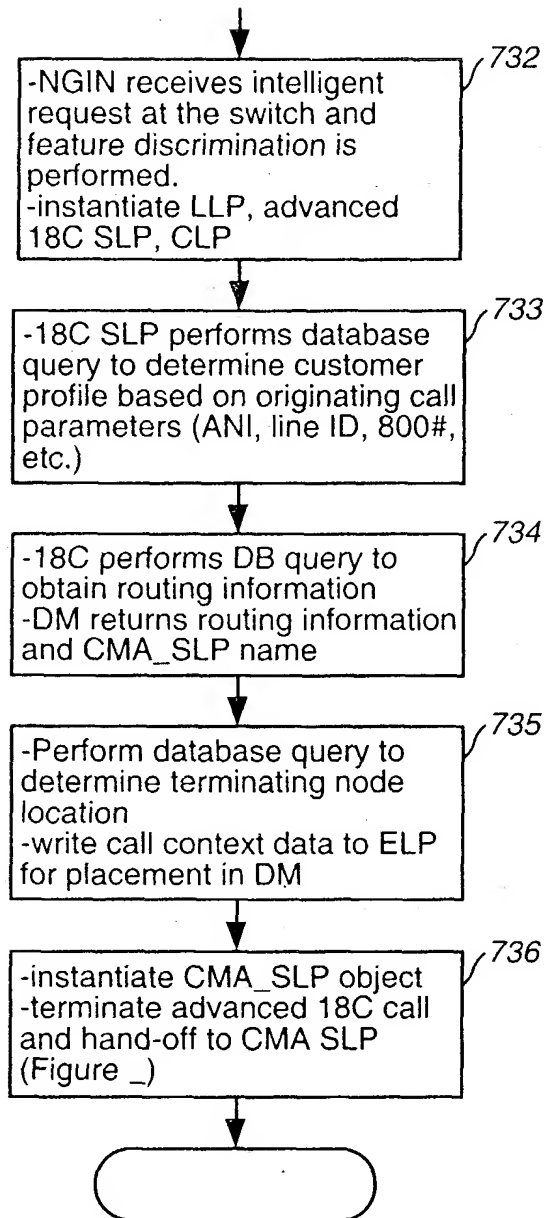


FIG. 12a

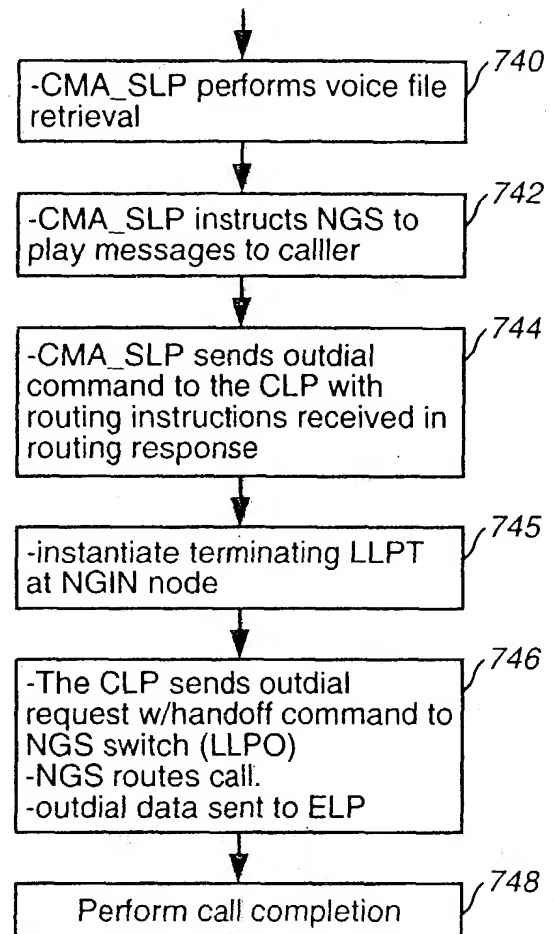
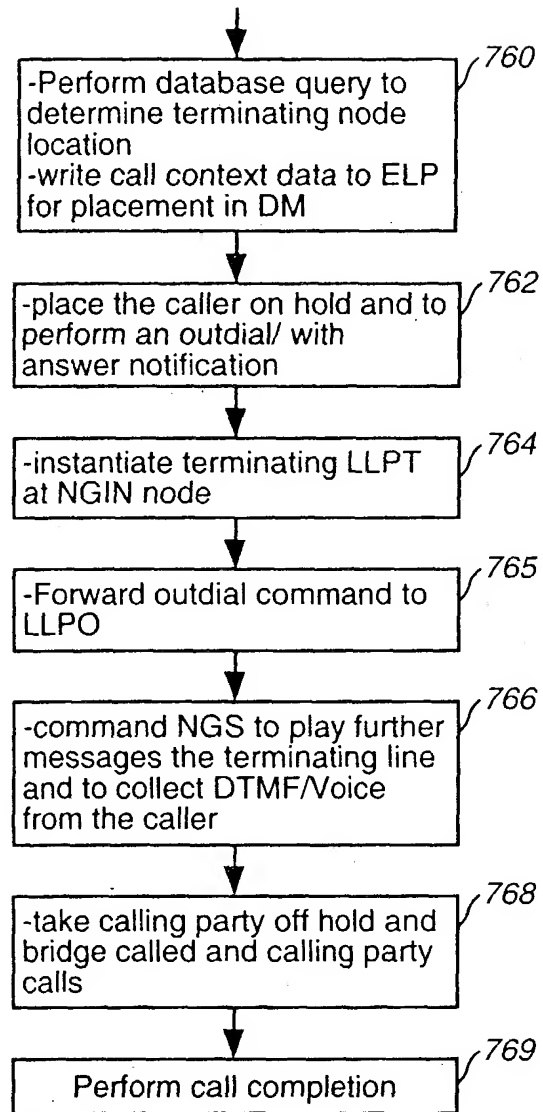
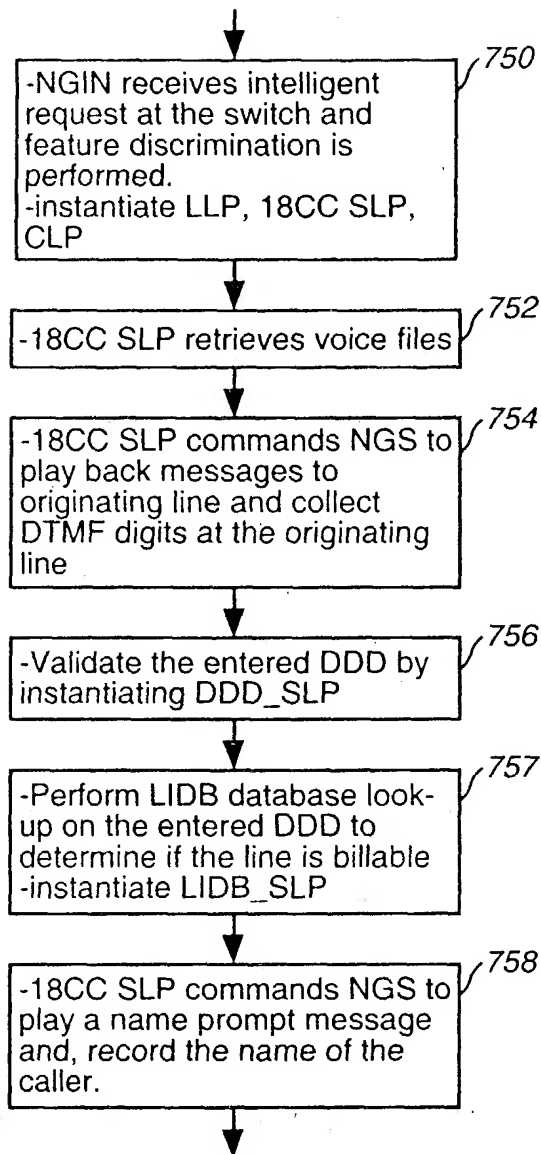
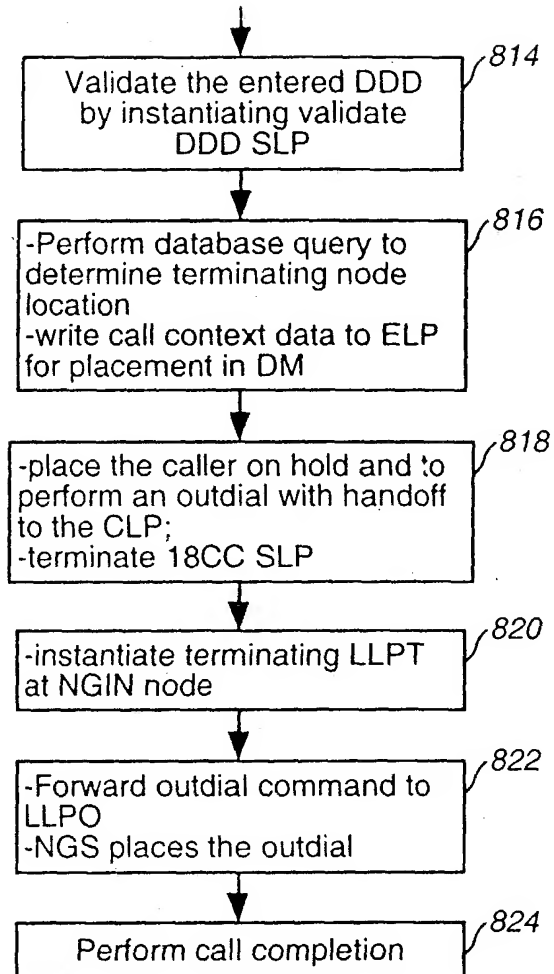
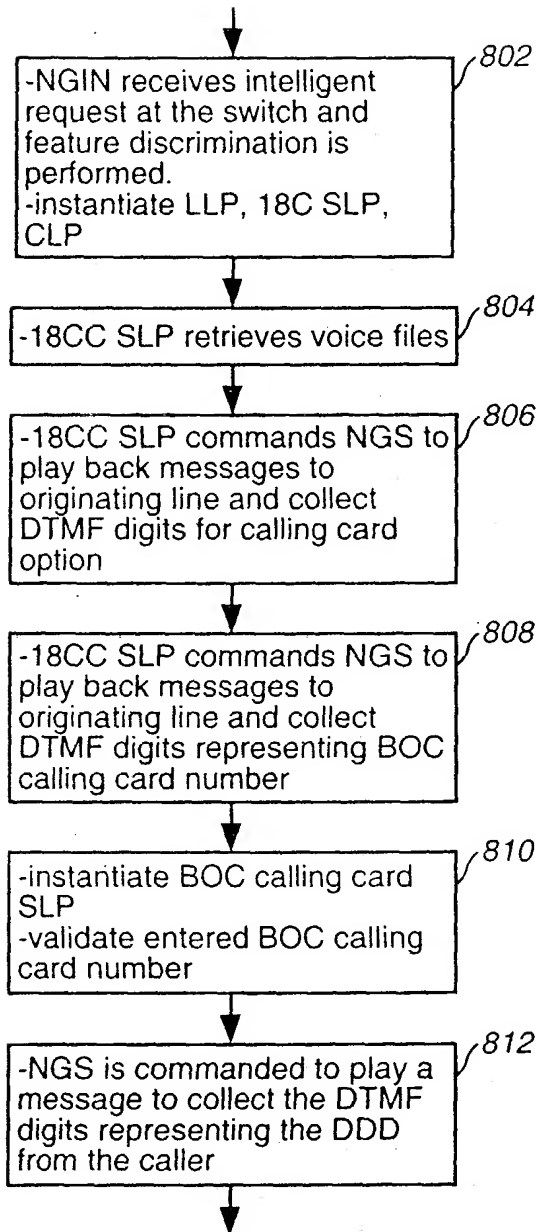


FIG. 12b

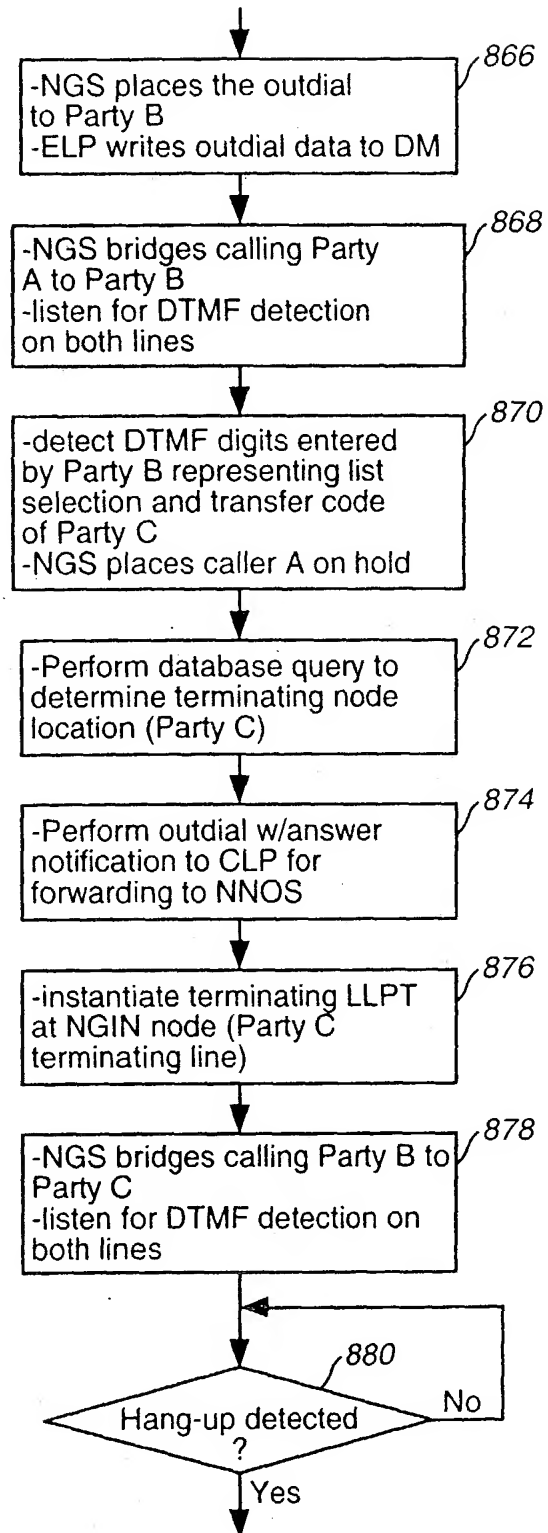
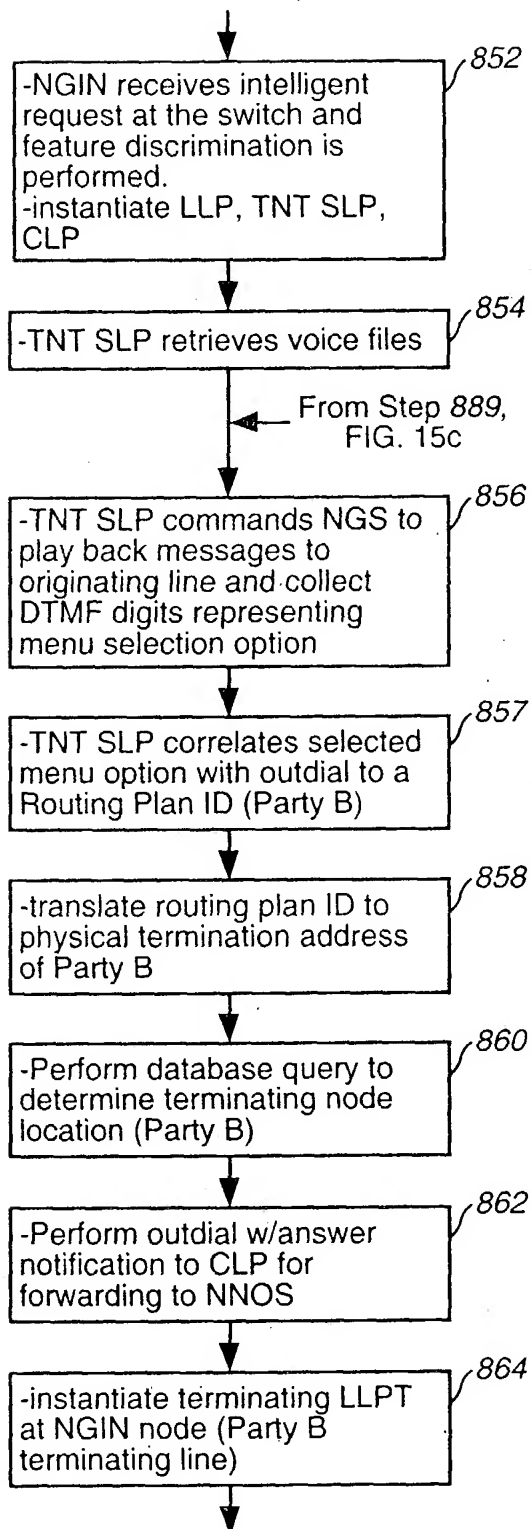
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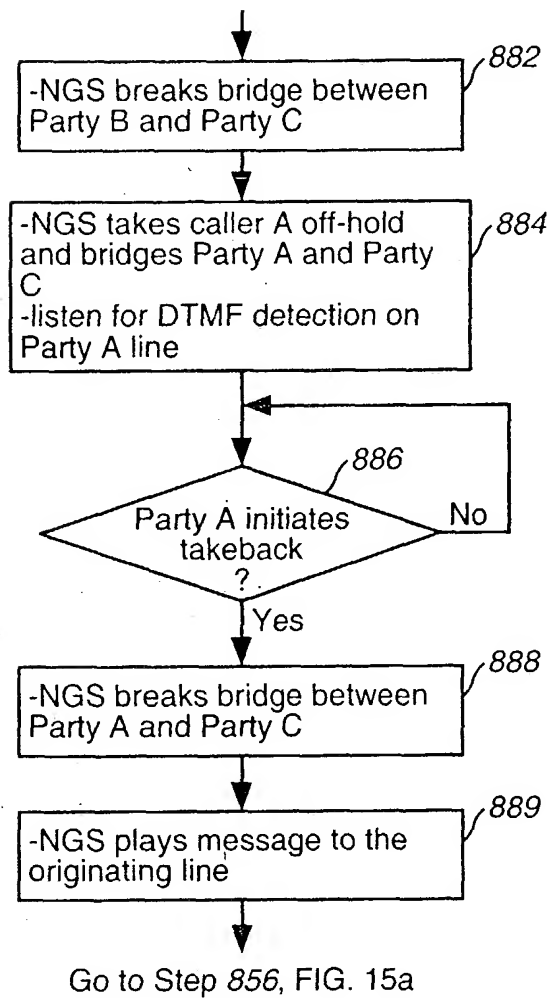


FIG. 16

